Tools for Assessing and Optimizing the Energy Requirements of High Performance Scientific Computing Software

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Outline

- Background
- Energy and Performance Analysis
- Outlook



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European

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Partners

















IT4Innovations national supercomputing center





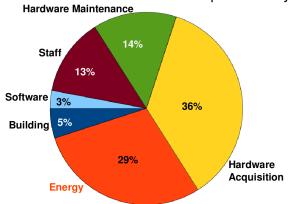
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Initial Observation

Distribution of Total Cost of Ownership of HPC System



Energy cost is already \sim 30% of total cost, with rising tendency.

Source: Survey conducted by RWTH Aachen (2012)



Challenges

- HPC centers
 - are forced to develop strategies for energy efficient modes of operation,
 - will ask users (e.g., the scientific computing community) to run their codes in an energy efficient manner.



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HPC centers

- are forced to develop strategies for energy efficient modes of operation,
- will ask users (e.g., the scientific computing community) to run their codes in an energy efficient manner.

HPC users

- are likely to be billed for energy instead of CPU time in the future.
- must find out how much energy their codes need to run,
- need to optimize their codes with respect to energy.



Possible Approach

LRZ München's Strategy for SuperMUC

Set default CPU frequency significantly lower than maximum. Allow use of higher frequency only if users can demonstrate that it pays off.



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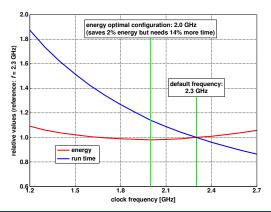
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- Model: $E = \int_0^T (P_{\text{static}}(t) + P_{\text{dynamic}}(t)) dt$
- P_{static} is constant (idle power)
- P_{dynamic} depends on frequency f and voltage U: $P_{\text{dynamic}} \sim U^2 \cdot f$
- Run time T is non-increasing function of frequency f



Observations

- When running an application with higher frequency . . .
 - static energy usually decreases (due to shorter run time)
 - dynamic energy usually increases
- Each application has its own optimal frequency!





When Does an Increased Frequency Pay Off?

Consequences of frequency reduction:

- Energy requirements are reduced
- Run time is increased
- Throughput of system is decreased
- Capabilities of hardware are not fully utilized
- More investement in hardware necessary to satisfy demand

Frequency increase pays off if run time decreases significantly

Pure focus on energy is not appropriate.



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Elementary Approach

Energy-delay product:

$$EDP = E \cdot T^w$$

- E: Energy used by the application run
- T: Run time used by the application run
- Parameter w is used to prioritize run time
- Usually $w \in \{1, 2, 3\}$ (EDP1, EDP2 and EDP3)
- A single metric to optimize both energy and hardware cost
 - \Rightarrow useful for our purpose in spite of physical implausibility



- Run code with a few typical example data sets and different frequencies
- Measure run times and energy requirements
- Select frequency with best EDP for production runs



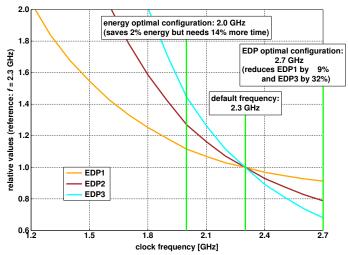
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Same approach can be followed for other tuning parameters:

- # of MPI tasks
- # of OpenMP threads
- ...

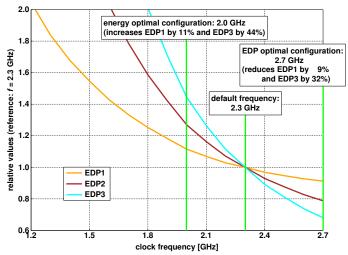


Example: Indeed (FE code for sheet metal forming simulation)



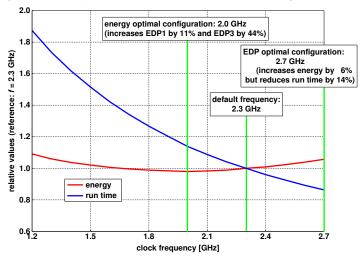


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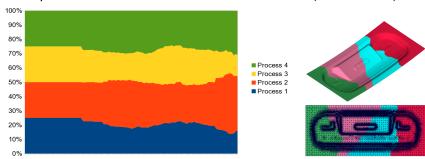


Dynamic Tuning

Fundamental Property

Many HPC codes exhibit dynamic behaviour.

Example: Distribution of workload in Indeed run (4 MPI tasks)





Dynamic Tuning

Exploit dynamic behaviour at run time:

- reduce CPU frequency for I/O bound or memory bound parts of code
- increase CPU frequency for compute bound parts of code
- reduce # of OpenMP threads in case of lock contention
- reduce # of MPI tasks if problem size is small or if algorithm scales poorly
- ...



Tools Landscape

Unified measurement infrastructure

Score-P

(http://www.score-p.org)



Tools Landscape

Analysis tools for performance

- CUBE: Profiling
- Scalasca: Automatic trace analysis
- Vampir: Interactive trace analysis
- TAU: Profiling and tracing
- Periscope Tuning Framework:
 On-line analysis and tuning

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Analysis tools for performance and energy requirements

- CUBE: Profiling
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- TAU: Profiling and tracing
- Periscope Tuning Framework: new: energy tuning plugins On-line analysis and tuning

new: visualization & analysis capabilities for energy related metrics

(PCAP, DVFS, MPIProcs, ...)

Unified measurement infrastructure

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new: interface to energy measurement hardware



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 - CPU frequency
 - different code paths
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- Identify certain scenarios at design time



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- Identify points in code where change of tuning parameters is reasonable
- Identify certain scenarios at design time
- Find energy-optimal configuration for continuation of program run





(Semi)-automatic energy tuning II: At run time

 Automatic switching between configurations at run time according to current scenario (via READEX runtime library)



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- User friendliness



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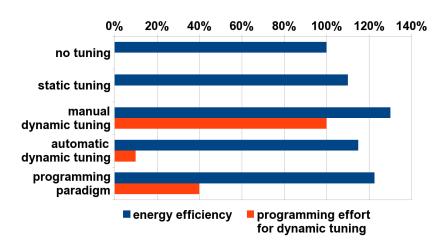
Future work:

Development of programming paradigm for expressing dynamism

Goal: further improvement of automatic dynamic tuning



Typical Outcome



Thank you for your attention!



Further

information: http://www.vi-hps.org/projects/score-e

http://www.readex.eu

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